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1 Advances in functional abstraction from structure

Richard H. Lathrop, Robert J. Hall, Gavan Duffy, K. Mark Alexander, Robert S. Kirk

June 1988 **Proceedings of the 25th ACM/IEEE conference on Design automation**

Full text available: pdf(537.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

FUNSTRUX has been extended to extract behavioral level models for a commercial simulator directly from a circuit netlist. Recent advances include: a retargetable code generation mechanism; an object-oriented control structure; handling of initialization values; and improved run-time and space requirements of the abstraction process. We discuss some of the issues that arise in translating from LISP to 'C' and from one functional paradigm to another.

2 An overview of Motorola's PowerPC simulator family

William Anderson

June 1994 **Communications of the ACM**, Volume 37 Issue 6

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1 TARGET: thermal accelerated reliability go-no-go environmental testing dynamic board thermal shock using a single liquid fluorocarbon bath

Beaton, B.P.;

Electronics Manufacturing Technology Symposium, 1991., Eleventh IEEE/CHMT International, 16-18 Sept. 1991

Pages:212 - 214

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Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 2 , 25-28 May 2003

Pages:II-808 - II-811 vol.2

[\[Abstract\]](#)[\[PDF Full-Text \(404 KB\)\]](#)

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2 Silicon debug: scan chains alone are not enough*Van Rootselaar, G.J.; Vermeulen, B.;*

Test Conference, 1999. Proceedings. International , 28-30 Sept. 1999

Pages:892 - 902

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In the implemented voice dialogue system "The Circuit Fix-it Shop" 19]the following dialogue
<ftp.cs.duke.edu/pub/cig/papers/hics.ps.Z>

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Microsoft Developer Studio Generated NMAKE File, Format Version 4.00 # ** DO NOT EDIT ** # TARGETTYPE " "\$(CFG)" == "" CFG=quantum - Win32 **Debug** !MESSAGE No configuration specified.
www.doc.ic.ac.uk/~ids/realdotdot/misc/projects/QuantumComputing/jon_m/report-and-source-code-from-ar ... - 26
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3. **Debug mode without Short-circuit** ⓘ
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4. [ICD.book \(PDF\)](#) ⓘ
TRACE 32In-Circuit DebuggerQuick Installation andTutorialTRACE32: In-Circuit Debugger (ICD) August 1998C Datentechnik GmbHAll rights reserved. ... What is an In-Circuit Debugger?Most CPUs provide an onchip **debug**
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6. <http://www.doc.ic.ac.uk/~ids/dotdot/misc/projects/QuantumComputing/daa98/report-and-source>
Configuration: QBit - Win32 **Debug** Beginning build with project "H:\Q_PROJECT\QB Win32 (x86) Application (based ...
www.doc.ic.ac.uk/~ids/dotdot/misc/projects/QuantumComputing/daa98/report-and-source-code-from-archiv ... - 3k
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medea.it.uc3m.es/medea_conference_files/slides/session%204/4.2.pdf - 212k - [View as html](#)
8. **In-Circuit Emulation: How the Microprocessor Evolved Over Time** ⓘ
In-Circuit Emulation: How the Microprocessor Evolved Over Time. By Robert R. Collins. Microprocessors weren't mind. ... these support pins, added a few more instructions, some **debug** registers, and a few special bus cycles .
www.rcollins.org/ddj/Sep97 - 22k - [Cached](#)


9. MS Embedded Review 
 ... Dramatically accelerate in-circuit validation of systems with FPGAs ... The Traditional In-Circuit Emulator. Tra
 equipment available to an ...
www.eetimes.com/review98/bottom4.fhtml - 96k - [Cached](#) - [More pages from this site](#)

10. Airline Product Set with MATIP 
 ... config-alps-circ)# alps enable-circuit (config)# alps circuit CKT_ALC_2 (config-alps ... is from the debug alps
 circuit event status change ...
akson.sgh.waw.pl/~chopin/ios120/120newft/120t/120t5/alpmatip.htm - 81k - [Cached](#) - [More pages from this site](#)

11. LAUTERBACH - TRACE32 ICD In-Circuit Debugger 
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 can be used to debug a ...
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 Background Debug ModeBKGND 1NC 32 ...
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 in-circuit emulator has full ...
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 ... Caution Do not enable the debug ncia circuit command during normal operation because this command gene
 the debug ncia circuit error command ...
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15. The Laboratory Staff will not help debug any circuit whose power supplies have not been prop
 Last Revision: August 20, 2000Page 1 of 14University of California at BerkeleyPhysics 111 LaboratoryBasic Sem
 II©2000 by the Regents of the University of California. ... offset triangle wave to debug your circuit. (The circuit
www.deas.harvard.edu/~jones/es154/lectures/lecture_1/pdfs/bsc8.pdf - 172k - [View as html](#)

16. The Laboratory Staff will not help debug any circuit whose power supplies have not been prop
 ... The lab ends with the construction of a complex circuit that calculates root mean squares ... The Laboratory St
 power supplies have not been ...
socrates.berkeley.edu/~phylabs/bsc/PDFFiles/bsc8.pdf - 248k - [View as html](#)

17. eX_core In-Circuit Emulation support (PDF) 
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 debug cable has only six ...
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18. iSYSTEM - In-circuit Emulators for Embedded Systems 
 iSYSTEM develops in-circuit emulators and debugging tools for embedded systems, microcontrollers MCU. ... su
 connects the iC3000 with an ActivePODTM for full in-circuit emulation ... Motorola's Background Debug Mode (B
www.isystem.com/news/Press/iC3000.htm - 28k - [Cached](#) - [More pages from this site](#)

19. Introduction 
 ... The debug flow is usually influenced by the functionality, hardware circuit type, hierarchy and other features o
 circuit or it may be ...
www.fe.up.pt/~allegro/wp3/d3x_wbm/GRA1/debug_flow_introduction.htm - [More pages from this site](#)

20. [Hitex Development Tools for the Embedded World / debugger/in-circuit emulators/software-too](#)
We develop and manufacture in-circuit emulators and debugging tools for embedded systems. ... RTOS Debug S
RTOS ... Instead, the in-circuit emulator uses its trace buffer for that purpose ...
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